

FIG. 1A

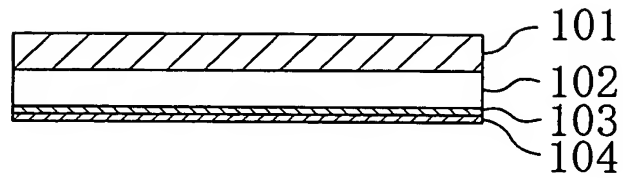


FIG. 1B

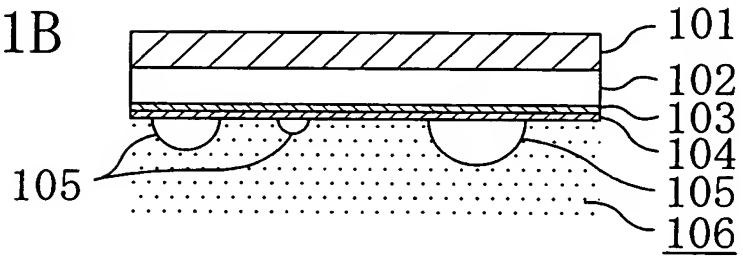


FIG. 1C [High-Speed-Rotation Wafer Debubbling Step]

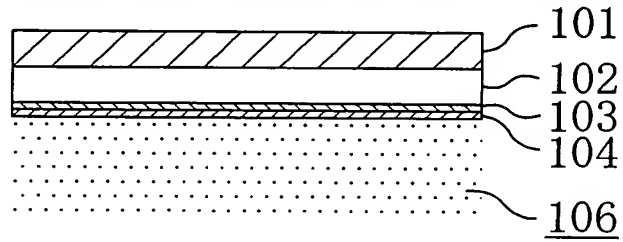


FIG. 1D

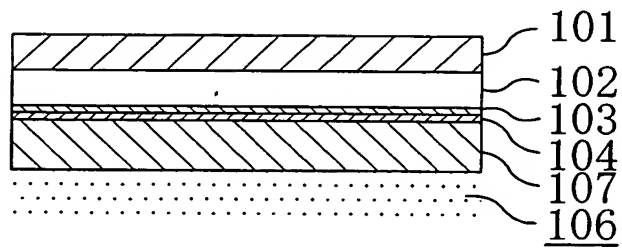


FIG. 1E

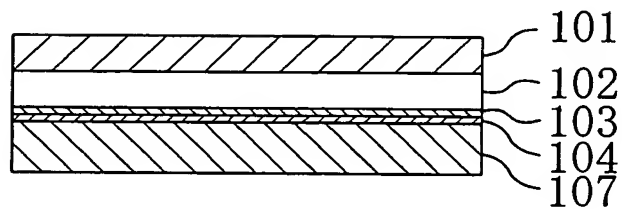


FIG. 2A

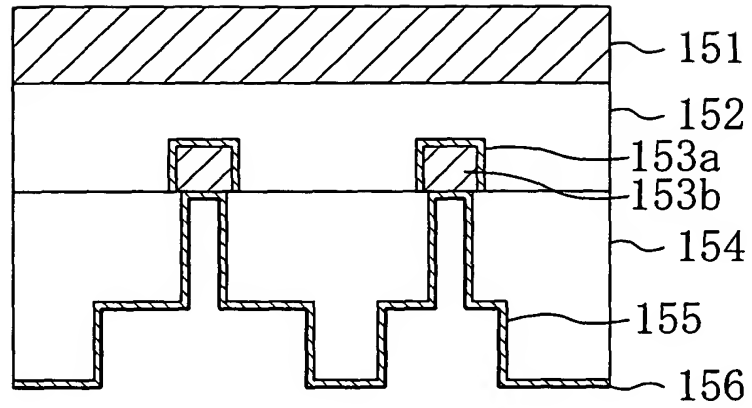


FIG. 2B

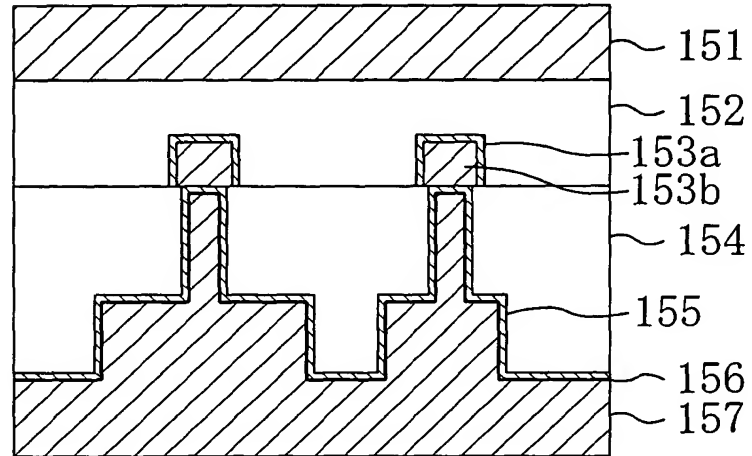


FIG. 2C

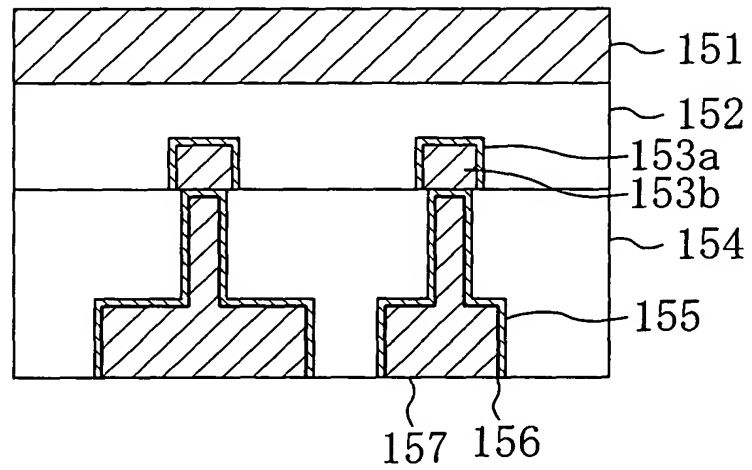
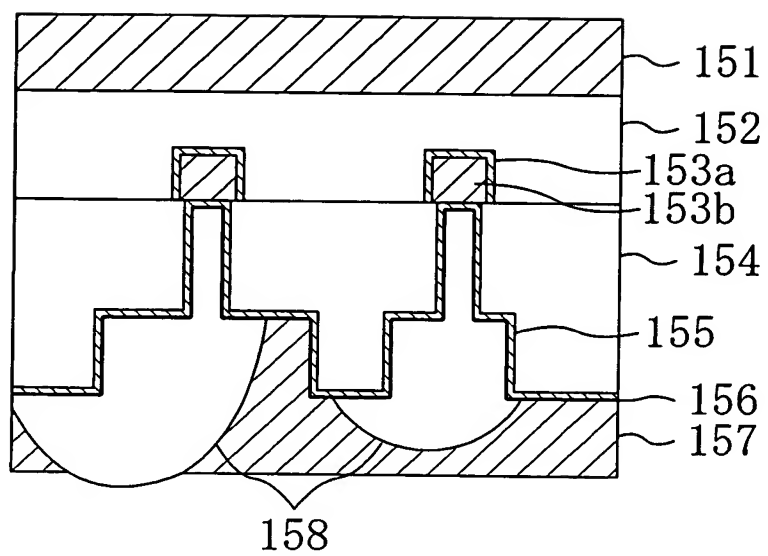
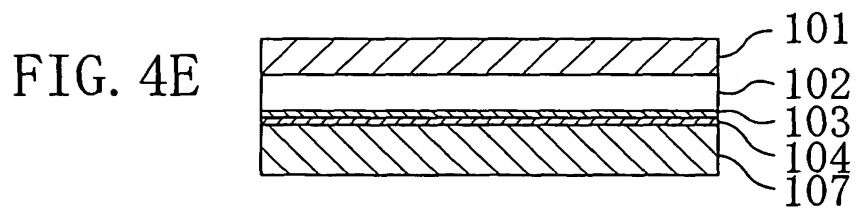
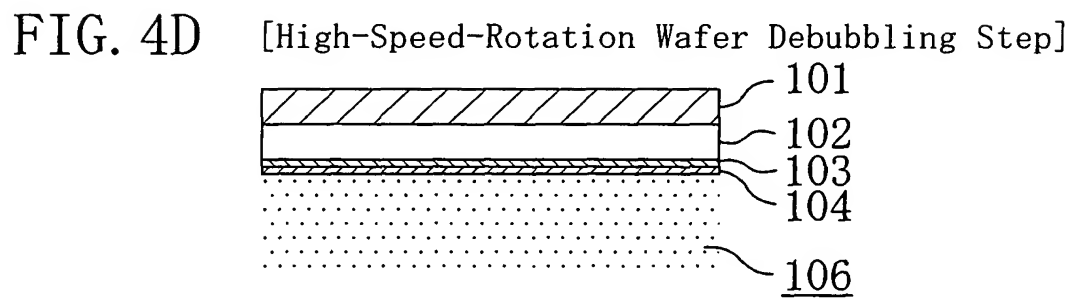
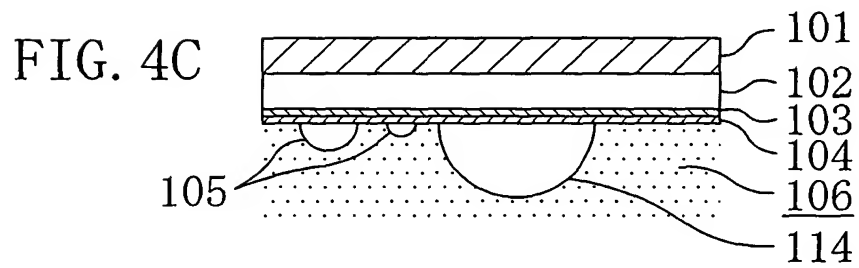
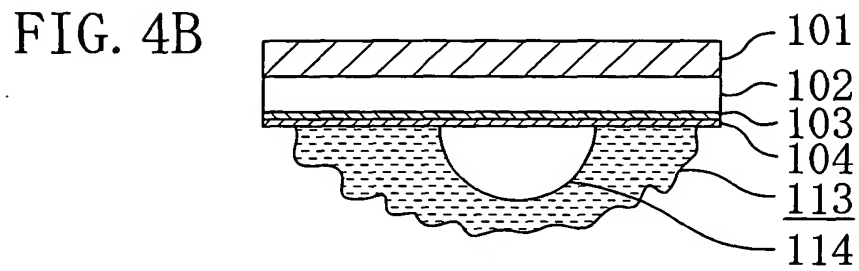
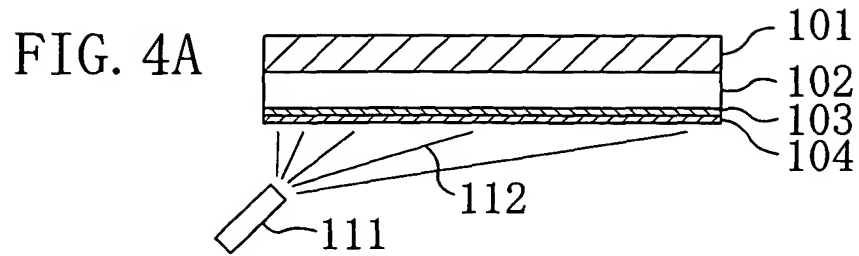


FIG. 3





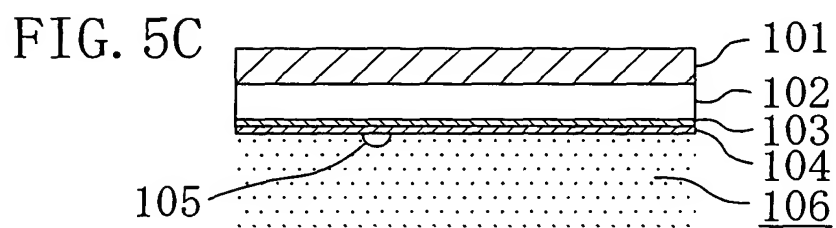
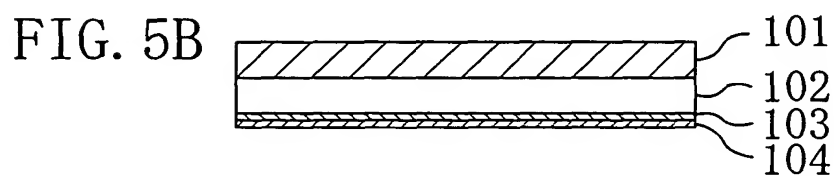
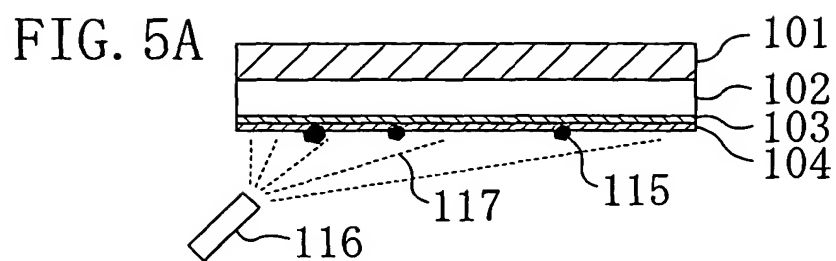


FIG. 5D [High-Speed-Rotation Wafer Debubbling Step]

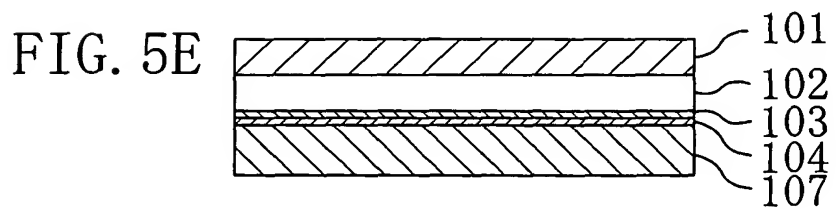
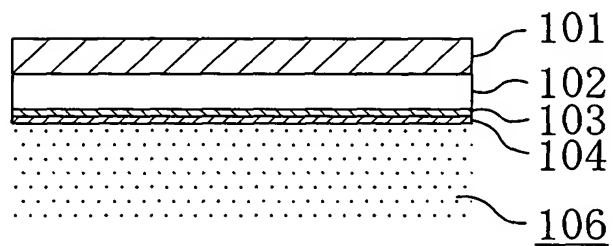


FIG. 6A

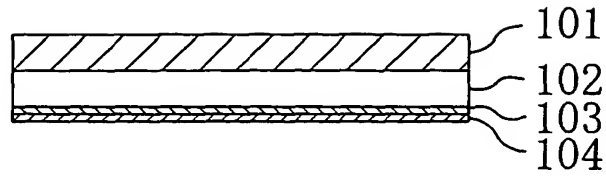


FIG. 6B

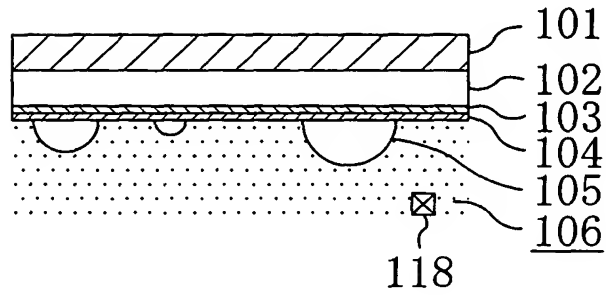


FIG. 6C

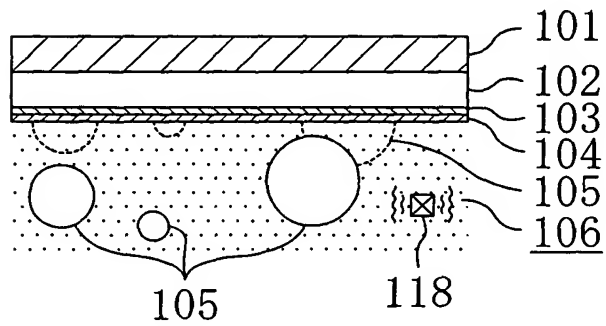


FIG. 6D [High-Speed-Rotation Wafer Debubbling Step]

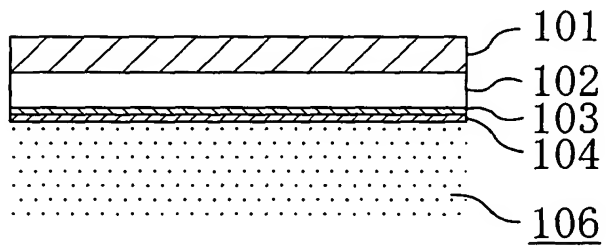


FIG. 6E

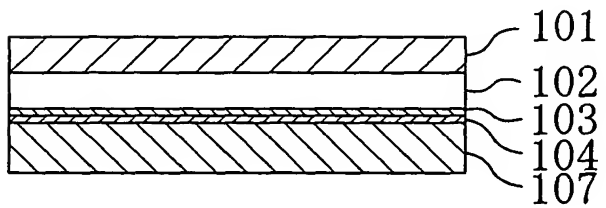


FIG. 7A

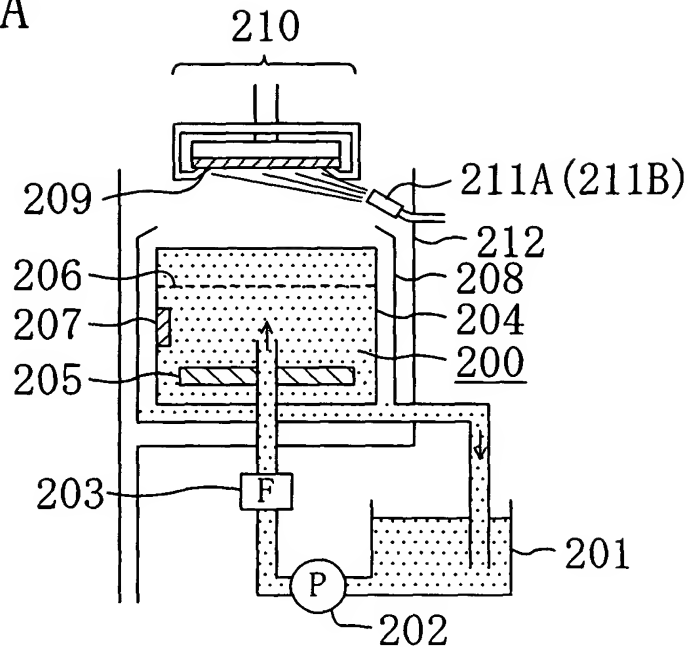


FIG. 7B

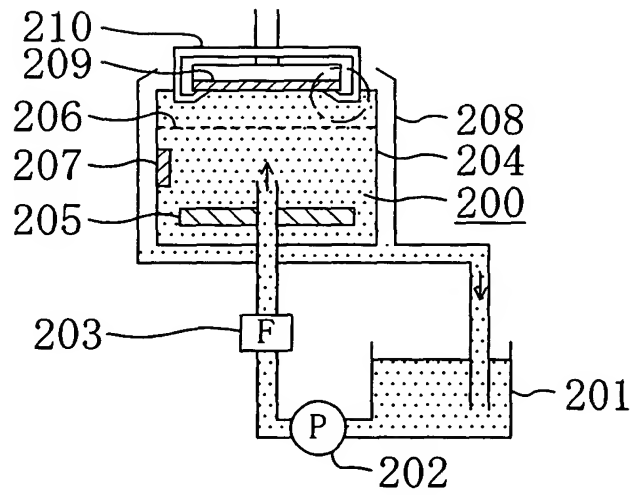


FIG. 8

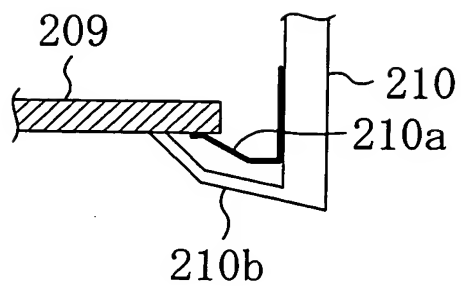


FIG. 9

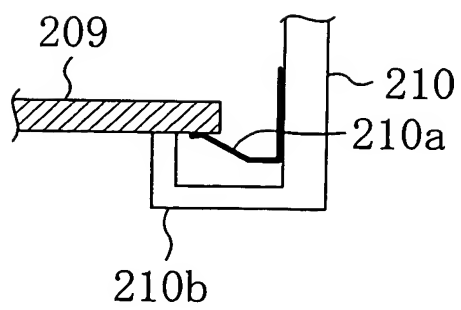


FIG. 10A
PRIOR ART

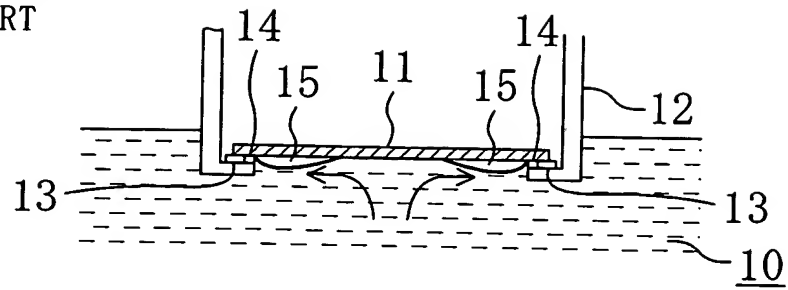


FIG. 10B
PRIOR ART

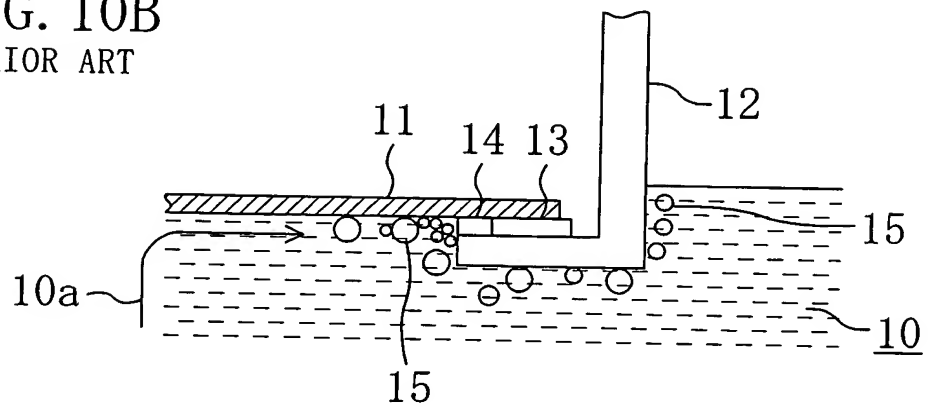


FIG. 10C
PRIOR ART

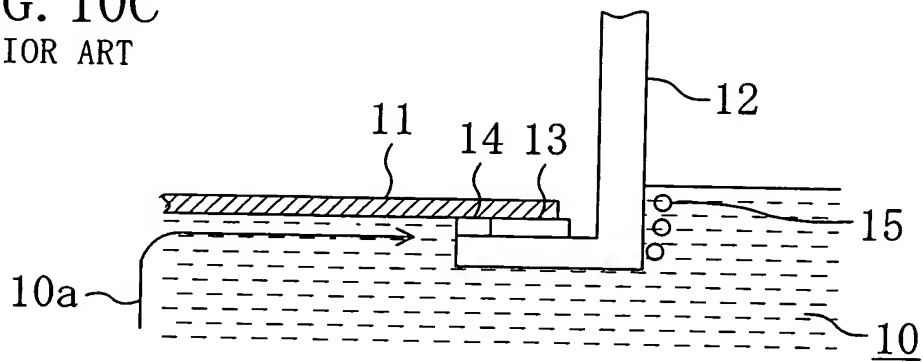


FIG. 11A
PRIOR ART

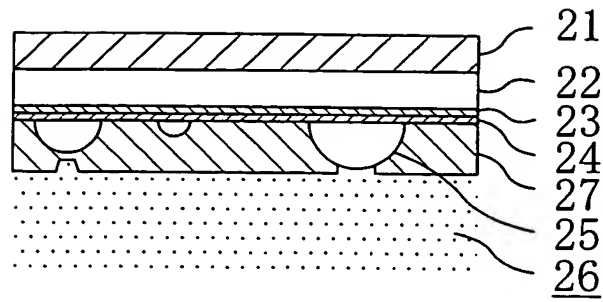


FIG. 11B
PRIOR ART

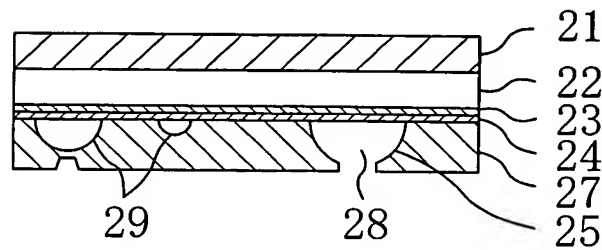


FIG. 12A
PRIOR ART

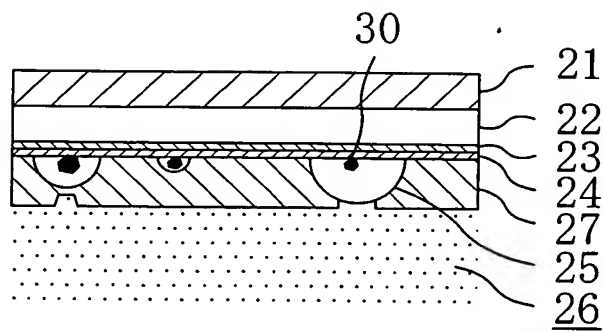


FIG. 12B
PRIOR ART

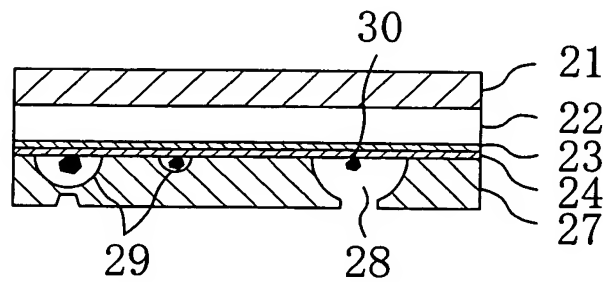


FIG. 13A
PRIOR ART

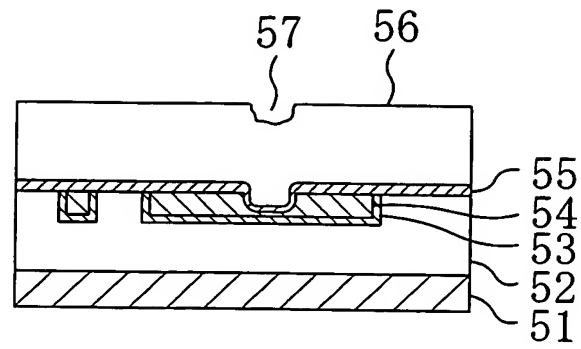


FIG. 13B
PRIOR ART

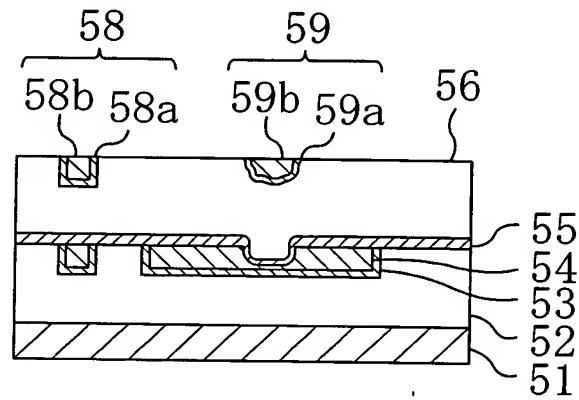


FIG. 13C
PRIOR ART

